

Amendments to the Claims :

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electronic circuit for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the circuit comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a first electrical signal on one of the second pair of conductors; and

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a second electrical signal on the other of the second pair of conductors,

the first and second electrical signal together constituting a loop back of the differential mode signal.

2. (Currently Amended) An electronic circuit for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the circuit comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors;

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors; and

~~A circuit in accordance with claim 1, further comprising:~~

circuitry responsive to application of a DC voltage level disabling the first steering circuit.

3. (Original) A circuit in accordance with claim 2, further comprising:
circuitry responsive to application of said DC voltage level disabling the second steering circuit.

4. (Currently Amended) An electronic circuit for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the circuit comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors; and

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors; and

~~A circuit in accordance with claim 1, further comprising:~~

circuitry responsive to application of a DC voltage level distorting the differential mode signal prior to transmitting it on the second pair of conductors.

5. (Currently Amended) An electronic circuit for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the circuit comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors; and

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors,

~~A circuit in accordance with claim 1~~

wherein said first steering circuit comprises:

an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

6. (Original) A circuit in accordance with claim 5 wherein said second steering circuit comprises an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

7. (Original) A circuit in accordance with claim 6, further comprising:
circuitry responsive to application of a DC voltage level disabling the first steering circuit.

8. (Original) A circuit in accordance with claim 7, further comprising:
circuitry responsive to application of said DC voltage level disabling the second steering circuit.

9. (Original) A circuit in accordance with claim 6, further comprising:
circuitry responsive to application of a DC voltage level distorting the differential mode signal prior to transmitting it on the second pair of conductors.

10. (Currently Amended) An electronic circuit for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the circuit comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors; and

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors.

~~A circuit in accordance with claim 1~~

wherein said first steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.

11. (Original) A circuit in accordance with claim 10 wherein said second steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.

12. (Original) A circuit in accordance with claim 11, further comprising:
circuitry responsive to application of a DC voltage level disabling the first steering circuit.

13. (Original) A circuit in accordance with claim 12, further comprising:
circuitry responsive to application of said DC voltage level disabling the second steering circuit.

14. (Original) A circuit in accordance with claim 11, further comprising:
circuitry responsive to application of a DC voltage level distorting the differential mode signal prior to transmitting it on the second pair of conductors.

15. (Currently Amended) An electronic circuit for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the circuit comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors;

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors;

~~A circuit in accordance with claim 1, further comprising:~~

a current mirror associated with said first steering circuit;

a voltage storage device coupled to said current mirror; and

a switch controlled by a voltage stored on said voltage storage device, said switch coupled to said second steering circuit for altering operation of said second steering circuit in response to the voltage stored on said voltage storage device.

16. (Currently Amended) An electronic circuit for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the circuit comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors;

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors;

~~A circuit in accordance with claim 1, further comprising:~~

a current mirror associated with said first steering circuit;

a voltage storage device coupled to said current mirror; and

a switch controlled by a voltage stored on said voltage storage device, said switch coupled to said first steering circuit for altering operations of said first steering circuit in response to the voltage stored on said voltage storage device.

17. (Original) A circuit in accordance with claim 15 wherein said first steering circuit comprises:

an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

18. (Original) A circuit in accordance with claim 16 wherein said first steering circuit comprises:

an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

19. (Original) A circuit in accordance with claim 17 wherein said second steering circuit comprises an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

20. (Original) A circuit in accordance with claim 18 wherein said second steering circuit comprises an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

21. (Original) A circuit in accordance with claim 15 wherein said first steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.

22. (Original) A circuit in accordance with claim 16 wherein said first steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.

23. (Original) A circuit in accordance with claim 21 wherein said second steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.

24. (Original) A circuit in accordance with claim 22 wherein said second steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.

25. (Currently Amended) A method for controlling the loop back of a differential mode signal received at a network device on a first pair of conductors and transmitted from the network device on a second pair of conductors, the method comprising:

receiving the differential mode signal;

applying the differential mode signal to a first steering circuit and a second steering circuit;

generating a first electrical signal to be transmitted on a first one of the second pair of conductors with said first steering circuit; and

generating a second electrical signal to be transmitted on a second one of the second pair of conductors with said second steering circuit,

the first and second electrical signal together constituting a loop back of the differential mode signal.

26. (Currently Amended) A method for controlling the loop back of a differential mode signal received at a network device on a first pair of conductors and transmitted from the network device on a second pair of conductors, the method comprising:

receiving the differential mode signal;

applying the differential mode signal to a first steering circuit and a second steering circuit;

generating a first signal to be transmitted on a first one of the second pair of conductors with said first steering circuit;

generating a second signal to be transmitted on a second one of the second pair of conductors with said second steering circuit; and

~~A method in accordance with claim 25, further comprising:~~

transmitting said first and second signals on the second pair of conductors.

27. (Original) A method in accordance with claim 26, further comprising:

receiving at the network device a power signal; and

disabling at least one of said first steering circuit and said second steering circuit in response to receipt of said power signal.

28. (Original) A method in accordance with claim 26, further comprising:
receiving at the network device a power signal; and
distorting at least one of said first signal and said second signal in response to receipt of
said power signal.
29. (Original) A method in accordance with claim 26, further comprising:
mirroring current from at least one of said first steering circuit and said second steering
circuit;
rectifying said mirrored current;
applying said rectified current to a voltage storage device;
using said voltage storage device to control at least one switch; and
disabling at least one of said first steering circuit and said second steering circuit with
said at least one switch.
30. (Original) A method in accordance with claim 26, further comprising:
mirroring current from at least one of said first steering circuit and said second steering
circuit;
rectifying said mirrored current;
applying said rectified current to a voltage storage device;
using said voltage storage device to control at least one switch; and
distorting at least one of said first signal and said second signal with said at least one
switch.

31. (Currently Amended) An apparatus for controlling the loop back of a differential mode signal received at a network device on a first pair of conductors and transmitted from the network device on a second pair of conductors, the apparatus comprising:

means for receiving the differential mode signal;

means for applying the differential mode signal to a first steering circuit and a second steering circuit;

means for generating a first electrical signal to be transmitted on a first one of the second pair of conductors with said first steering circuit; and

means for generating a second electrical signal to be transmitted on a second one of the second pair of conductors with said second steering circuit,

the first and second electrical signal together constituting a loop back of the differential mode signal.

32. (Currently Amended) An apparatus for controlling the loop back of a differential mode signal received at a network device on a first pair of conductors and transmitted from the network device on a second pair of conductors, the apparatus comprising:

means for receiving the differential mode signal;

means for applying the differential mode signal to a first steering circuit and a second steering circuit;

means for generating a first signal to be transmitted on a first one of the second pair of conductors with said first steering circuit;

means for generating a second signal to be transmitted on a second one of the second pair of conductors with said second steering circuit; and

An apparatus in accordance with claim 31, further comprising:

means for transmitting said first and second signals on the second pair of conductors.

33. (Original) An apparatus in accordance with claim 32, further comprising:
means for receiving at the network device a power signal; and
means for disabling at least one of said first steering circuit and said second steering circuit in response to receipt of said power signal.
34. (Original) An apparatus in accordance with claim 32, further comprising:
means for receiving at the network device a power signal; and
means for distorting at least one of said first signal and said second signal in response to receipt of said power signal.
35. (Original) An apparatus in accordance with claim 32, further comprising:
means for mirroring current from at least one of said first steering circuit and said second steering circuit;
means for rectifying said mirrored current;
means for applying said rectified current to a voltage storage device;
means for using said voltage storage device to control at least one switch; and
means for disabling at least one of said first steering circuit and said second steering circuit with said at least one switch.
36. (Original) An apparatus in accordance with claim 32, further comprising:
means for mirroring current from at least one of said first steering circuit and said second steering circuit;

means for rectifying said mirrored current;

means for applying said rectified current to a voltage storage device;

means for using said voltage storage device to control at least one switch; and

means for distorting at least one of said first signal and said second signal with said at least one switch.

37. (Currently Amended) A system including a voice over IP telephone switch and at least one voice over IP telephone, the telephone including an apparatus for controlling the loop back of a differential mode signal received at the telephone on a first pair of conductors and transmitted from the telephone on a second pair of conductors, the apparatus comprising:

means for receiving the differential mode signal;

means for applying the differential mode signal to a first steering circuit and a second steering circuit;

means for generating a first electrical signal to be transmitted on a first one of the second pair of conductors with said first steering circuit; and

means for generating a second electrical signal to be transmitted on a second one of the second pair of conductors with said second steering circuit,

the first and second electrical signal together constituting a loop back of the differential mode signal.

38. (Currently Amended) A system including a voice over IP telephone switch and at least one voice over IP telephone, the telephone including an apparatus for controlling the loop back of a differential mode signal received at the telephone on a first pair of conductors and transmitted from the telephone on a second pair of conductors, the apparatus comprising:

means for receiving the differential mode signal;

means for applying the differential mode signal to a first steering circuit and a second steering circuit;

means for generating a first signal to be transmitted on a first one of the second pair of conductors with said first steering circuit;

means for generating a second signal to be transmitted on a second one of the second pair of conductors with said second steering circuit; and

~~A system in accordance with claim 37, wherein the apparatus further comprises:~~

means for transmitting said first and second signals on the second pair of conductors.

39. (Previously presented) A system in accordance with claim 38, wherein the apparatus further comprises:

means for receiving at the telephone a power signal; and

means for disabling at least one of said first steering circuit and said second steering circuit in response to receipt of said power signal.

40. (Previously presented) A system in accordance with claim 38, wherein the apparatus further comprises:

means for receiving at the telephone a power signal; and

means for distorting at least one of said first signal and said second signal in response to receipt of said power signal.

41. (Previously presented) A system in accordance with claim 38, wherein the apparatus further comprises:

means for mirroring current from at least one of said first steering circuit and said second steering circuit;

means for rectifying said mirrored current;

means for applying said rectified current to a voltage storage device;

means for using said voltage storage device to control at least one switch; and

means for disabling at least one of said first steering circuit and said second steering circuit with said at least one switch.

42. (Previously presented) A system in accordance with claim 38, wherein the apparatus further comprises:

means for mirroring current from at least one of said first steering circuit and said second steering circuit;

means for rectifying said mirrored current;

means for applying said rectified current to a voltage storage device;

means for using said voltage storage device to control at least one switch; and

means for distorting at least one of said first signal and said second signal with said at least one switch.

43. (Currently Amended) A system including a voice over IP telephone switch and at least one voice over IP telephone, the voice over IP telephone device for receiving a differential mode signal on a first pair of conductors and transmitting a loop back signal on a second pair of conductors, the device comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a first electrical signal on one of the second pair of conductors; and

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a second electrical signal on the other of the second pair of conductors, the first and second electrical signal together constituting a loop back of the differential mode signal.

44. (Currently Amended) A system including a voice over IP telephone switch and at least one voice over IP telephone, the voice over IP telephone device for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the device comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors;

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors; and

~~A system in accordance with claim 43, wherein said device further comprises:~~

~~circuitry responsive to application of a DC voltage level disabling the first steering circuit.~~

45. (Previously presented) A system in accordance with claim 44, wherein said device further comprises:

circuitry responsive to application of said DC voltage level disabling the second steering circuit.

46. (Currently Amended) A system including a voice over IP telephone switch and at least one voice over IP telephone, the voice over IP telephone device for receiving a differential mode

signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the device comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors;

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors; and

~~A system in accordance with claim 43, wherein said device further comprises:~~

circuitry responsive to application of a DC voltage level distorting the differential mode signal prior to transmitting it on the second pair of conductors.

47. (Currently Amended) A system including a voice over IP telephone switch and at least one voice over IP telephone, the voice over IP telephone device for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the device comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors;

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors; and

~~A system in accordance with claim 43 wherein said first steering circuit comprises:~~

an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

48. (Previously presented) A system in accordance with claim 47 wherein said second steering circuit comprises an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

49. (Previously presented) A system in accordance with claim 48, wherein said device further comprises:

circuitry responsive to application of a DC voltage level disabling the first steering circuit.

50. (Previously presented) A system in accordance with claim 49, wherein said device further comprises:

circuitry responsive to application of said DC voltage level disabling the second steering circuit.

51. (Previously presented) A system in accordance with claim 48, wherein said device further comprises:

circuitry responsive to application of a DC voltage level distorting the differential mode signal prior to transmitting it on the second pair of conductors.

52. (Currently Amended) A system including a voice over IP telephone switch and at least one voice over IP telephone, the voice over IP telephone device for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the device comprising:

a first steering circuit responsive to the differential mode signal on the first pair of
conductors for generating a signal on one of the second pair of conductors; and
a second steering circuit responsive to the differential mode signal on the first pair of
conductors for generating a signal on the other of the second pair of conductors.

~~A system in accordance with claim 43~~

wherein said first steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and
sources mutually coupled.

53. (Previously presented) A system in accordance with claim 52 wherein said second steering
circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and
sources mutually coupled.

54. (Previously presented) A system in accordance with claim 53, wherein said device further
comprises:

circuitry responsive to application of a DC voltage level disabling the first steering
circuit.

55. (Previously presented) A system in accordance with claim 54, wherein said device further
comprises:

circuitry responsive to application of said DC voltage level disabling the second steering
circuit.

56. (Previously presented) A system in accordance with claim 53, wherein said device further comprises:

circuitry responsive to application of a DC voltage level distorting the differential mode signal prior to transmitting it on the second pair of conductors.

57. (Currently Amended) A system including a voice over IP telephone switch and at least one voice over IP telephone, the voice over IP telephone device for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the device comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors;

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors;

~~A system in accordance with claim 43, wherein said device further comprises:~~

a current mirror associated with said first steering circuit;

a voltage storage device coupled to said current mirror; and

a switch controlled by a voltage stored on said voltage storage device, said switch coupled to said second steering circuit for altering operation of said second steering circuit in response to the voltage stored on said voltage storage device.

58. (Currently Amended) A system including a voice over IP telephone switch and at least one voice over IP telephone, the voice over IP telephone device for receiving a differential mode signal on a first pair of conductors and transmitting a signal on a second pair of conductors, the device comprising:

a first steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on one of the second pair of conductors;

a second steering circuit responsive to the differential mode signal on the first pair of conductors for generating a signal on the other of the second pair of conductors;

~~A system in accordance with claim 43, wherein said device further comprises:~~

a current mirror associated with said first steering circuit;

a voltage storage device coupled to said current mirror; and

a switch controlled by a voltage stored on said voltage storage device, said switch coupled to said first steering circuit for altering operations of said first steering circuit in response to the voltage stored on said voltage storage device.

59. (Previously presented) A system in accordance with claim 57 wherein said first steering circuit comprises:

an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

60. (Previously presented) A system in accordance with claim 58 wherein said first steering circuit comprises:

an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

61. (Previously presented) A system in accordance with claim 59 wherein said second steering circuit comprises an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

62. (Previously presented) A system in accordance with claim 60 wherein said second steering circuit comprises an NPN bipolar transistor and a PNP bipolar transistor having their respective emitters and collectors mutually coupled.

63. (Previously presented) A system in accordance with claim 57 wherein said first steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.

64. (Previously presented) A system in accordance with claim 58 wherein said first steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.

65. (Previously presented) A system in accordance with claim 63 wherein said second steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.

66. (Previously presented) A system in accordance with claim 64 wherein said second steering circuit comprises:

a P-channel MOSFET and an N-channel MOSFET having their respective drains and sources mutually coupled.